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PAGE 02/03

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Kristin R. Hayes

(Depositor's name)

*KRHAYES*

(Signature)

April 11, 2005

(Date)

04/12/2005 HGBREH2 00000016 501065 10759103

01 FC:1501 1400.00 DA  
02 FC:1504 300.00 DA

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10759103	01/20/2004	Maksim Kuzmenko	INF-129	6925

TITLE OF INVENTION: BUFFER AMPLIFIER ARCHITECTURE FOR SEMICONDUCTOR MEMORY CIRCUITS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	04/14/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
DINH, SON T	2824	365-189050

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  
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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,  
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Infineon Technologies AG

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Munich, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent):

☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Typed or printed name Ira S. Matsil

Date April 11, 2005

Registration No. 35,272

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PAGE 01/03

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## FACSIMILE COVER SHEET

To Commissioner for Patents	Total Pages Sent: 3
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: (703) 746-4000	Transmission Date: April 11, 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kuzmenka, *et al.* Docket No.: INF-129  
Serial No: 10/759,103 Art Unit: 2824  
Date Filed: January 20, 2004  
Title: Buffer Amplifier Architecture for Semiconductor Memory Circuits

## CERTIFICATION OF FACSIMILE TRANSMISSION

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- Certification of Facsimile Transmission (1 page)
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Respectfully submitted,

Kristin R. Hayes  
Legal Assistant

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